10/689,348

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Stephan Grunow, et al.

Serial No.:

10/689,348

Filed:

October 20, 2003

Title:

LOW K DIBLECTRIC INTEGRATED CIRCUIT

INTERCONNECT STRUCTURE

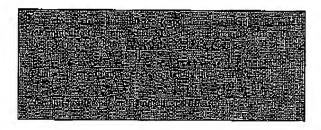
Grp./A.U.:

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Examiner:

Dana Farahani

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450



Sir:

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.116

In response to the Examiner's Action mailed January 25, 2006, the Applicants respectfully request reconsideration in view of the following remarks.